

## P500/BP500

The system is a fly-back circuit, in a half bridge configuration with quasi resonance switching. In the input a RFI filter (1) is implemented to reduce the reactive noise on the input leads. The rectification and the polarity protection are done by the rectifier bridge (2). The NTC resistor (3) limits the inrush current. The input capacitor  $C_{in}$  (5) smooths the rectifier voltage. Controlled by the control card (11), the transistor Q1 and Q2 supplying the transformer (9) in a PWM mode. On the secondary side of the transformer the voltage is rectified by the use of Dout (10) and smoothed by  $C_{out}$  (12). The output filter (13) reduces the RFI and ripple voltage on the output. The  $R_s$  (16) is monitoring the output current and set the current limit. The current limit characteristic is a UI mode.

The ORing diode (14) serve for polarity protection, in parallel and redundant operation.

The alarm (17) is a potential free contact, and indicates a low voltage on the output.

**Available Options:** Sense Lines.

